

产品规格书

Specification of products

产品名称:可控硅模块

产品型号: SKKT106A/16E-T02

浙江世菱半导体有限公司
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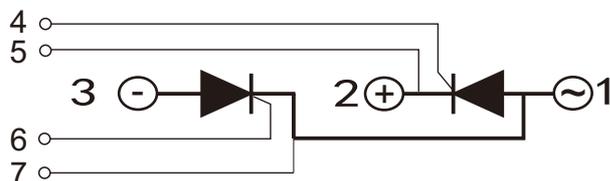
Http://www.smrshiling.com

拟制	审核	核准
林益龙	曹剑龙	宗瑞

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180 half sine wave 50Hz Single side cooled, T _c =85 C	125			106	A
I _{T(RMS)}	RMS on-state current		125			166	A
V _{DRM} V _{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	V _{DRM} & V _{RRM} tp=10ms V _{D5M} & V _{R5M} = V _{DRM} & V _{RRM} +200V	125	600		1800	V
I _{DRM} I _{RRM}	Repetitive peak current	at V _{DRM} at V _{RRM}	125			12	mA
I _{TSM}	Surge on-state current	10ms half sine wave	125			2.20	KA
I ² t	I ² T for fusing coordination	V _R =60%V _{RRM}				24.2 A	² ₅ *10 ³
V _{TO}	Threshold voltage		125			0.8	V
r _T	On-state slop resistance					2.29	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =330A	25			1.50	V
dv/dt	Critical rate of rise of off-state voltage V	V _{DM} =67%V _{DRM}	125			800	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t _r ≤ 0.5 μs Repetitive	125			100	A/μs
I _{GT}	Gate trigger current		25	30		100	mA
V _{GT}	Gate trigger voltage	V _A =12V, I _A =1A		1.0		2.5	V
I _H	Holding current			20		120	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125	0.2			V
R _{th(j-c)}	Thermal resistance Junction to case	Single side cooled per chip				0.250	C /W
R _{th(c-h)}	Thermal resistance case to heatsink	Single side cooled per chip				0.15	C /W
V _{iso}	Isolation voltage	50Hz,R.M.S,t=1min,I _{iso} :1mA(MAX)		2500			V
F _m	Thermal connection torque(M5)				4.0		N m
	Mounting torque(M6)				5.0		N m
T _{stg}	Stored temperature			-40		125	C
W _t	Weight				122		g
Outline	T-02						

OUTLINE DRAWING & CIRCUIT DIAGRAM

SKKT :



Rating and Characteristic

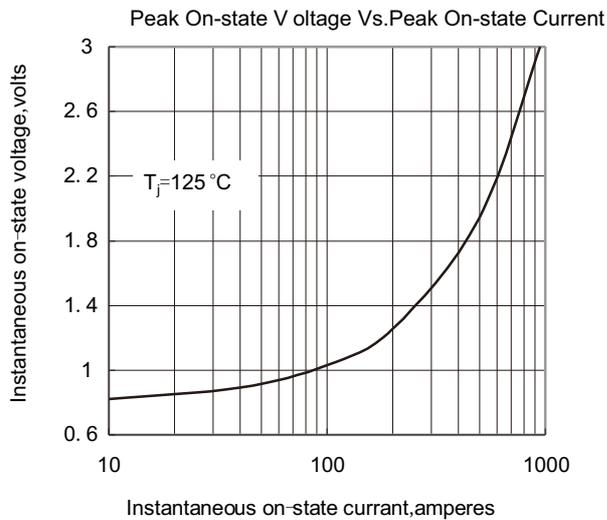


Fig. 1

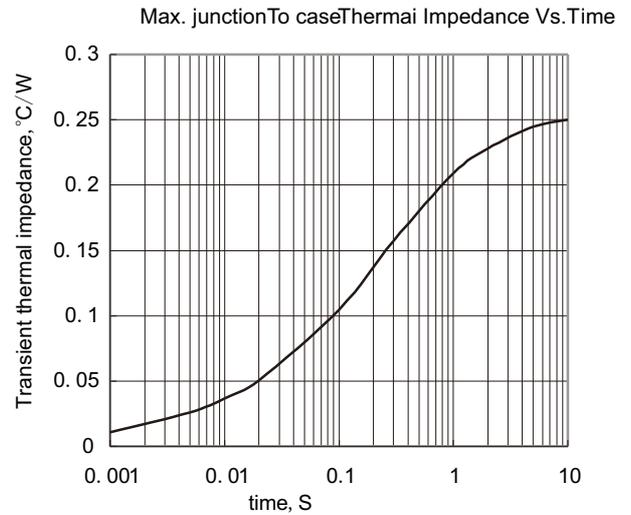


Fig. 2

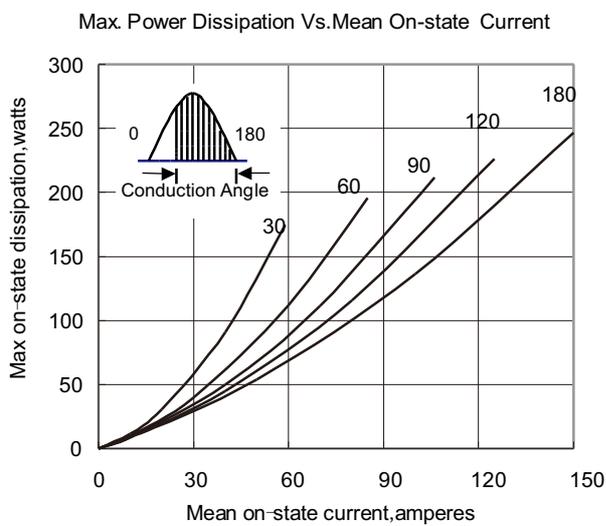


Fig. 3

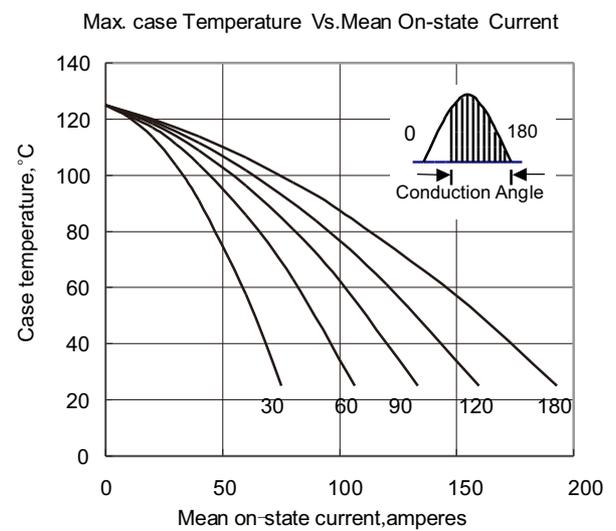


Fig. 4

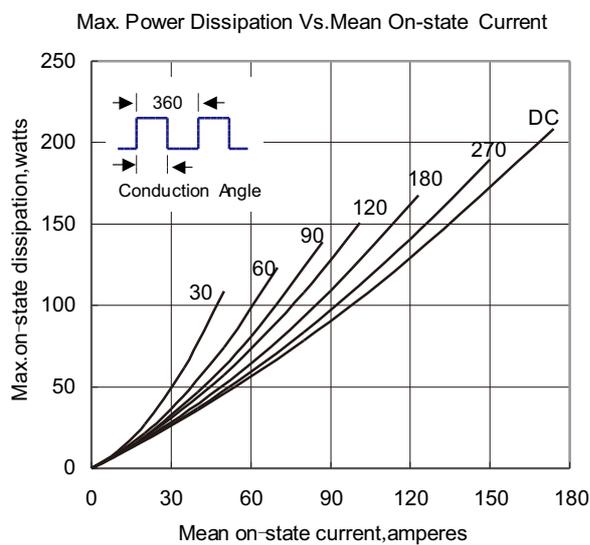


Fig. 5

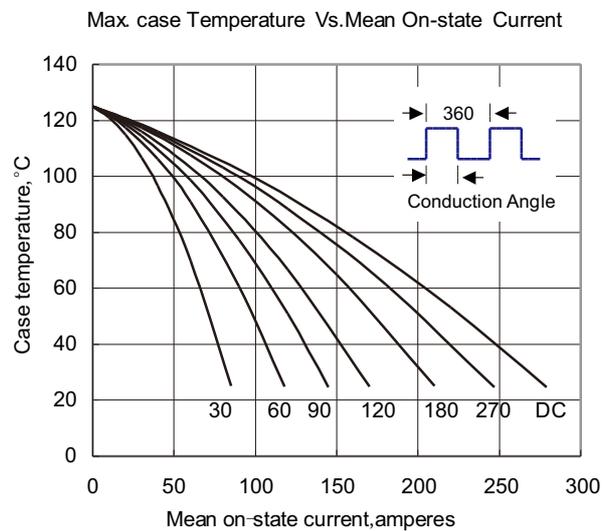


Fig. 6

Rating and Characteristic

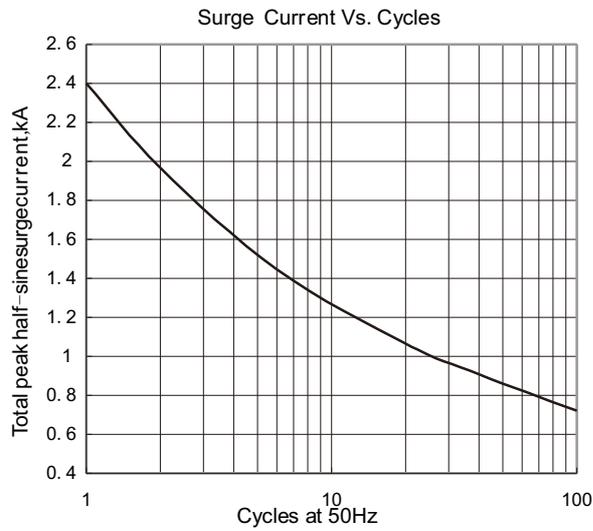


Fig. 7

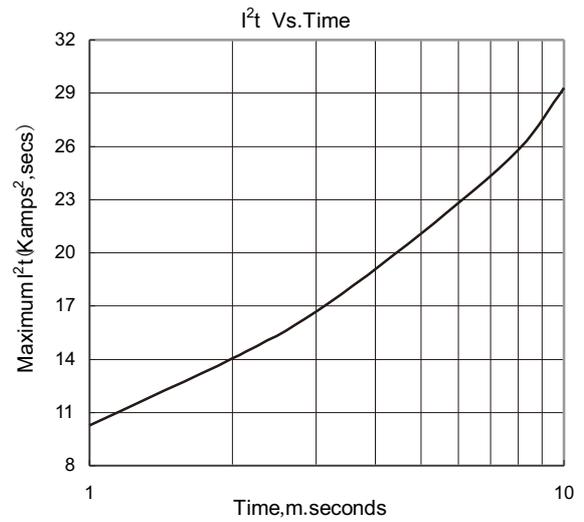


Fig. 8

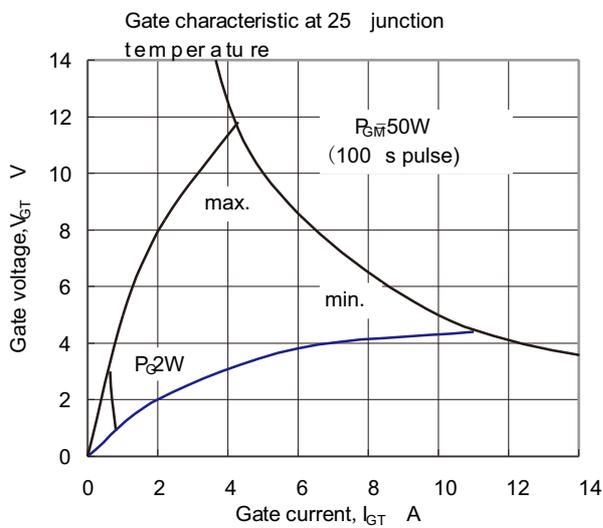


Fig. 9

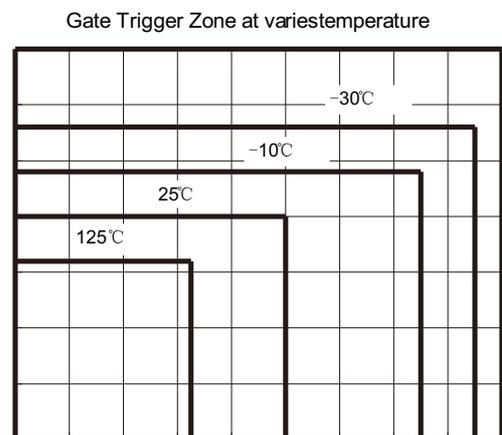


Fig. 10

Outside Dimension

